



XA-9612

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hiroshi INAGAWA et al.

Serial No.: 10/046,077

Group Art Unit: 2813

Filed: January 16, 2002

Examiner: D. Blum

For: INSULATED GATE TYPE SEMICONDUCTOR DEVICE AND METHOD  
FOR FABRICATING THE SAME

#2  
Election  
J. McWilliam  
10/28/03

\* \* \*

RESPONSE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

RECEIVED  
OCT 22 2003  
TECHNOLOGY CENTER 2800

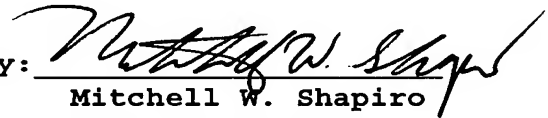
Responding to the Office Action mailed August 15,  
2003, and to the requirement for restriction therein,  
Applicants elect Group I (Claims 1-17), reserving the right  
to file a divisional application with regard to the non-  
elected invention.

An action on the merits of the claims to the elected  
invention is respectfully requested.

The Commissioner is hereby authorized to charge to  
Deposit Account No. 50-1165 any fees under 37 C.F.R. §§1.16  
and 1.17 which may be required by this paper, and to credit  
any overpayment to that Account. If any extension of time

is required in connection with the filing of this paper and  
has not been requested separately, then such extension is  
hereby requested.

Respectfully submitted,

By:   
Mitchell W. Shapiro  
Reg. No. 31,568

MWS:lat

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October 14, 2003